


Class	Subclass	ISSUE CLASSIFICATION
		

PATENT NUMBER

U.S. UTILITY Patent Application

④	NS	O.I.P.E.	PATENT DATE
SCANNED	AA	Q.A.	ne

APPLICATION NO. 09/497533	CONT/PRIOR YES	CLASS 710 712	SUBCLASS ² 215	ART UNIT 2781 2409	EXAMINER Harkness
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APPLICANTS

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TITLE

A Find-instructions-and-allocate-ports (FIAP) circuit and method for an out of order processor

PTO-2040
12/89**ISSUING CLASSIFICATION**

ORIGINAL							CROSS REFERENCE(S)									
CLASS				SUBCLASS			CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)								
INTERNATIONAL CLASSIFICATION																

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No: _____ _____ _____	_____ (Primary Examiner)		ISSUE FEE	
	_____ (Date)		Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		ISSUE BATCH NUMBER	
		_____ (Date)		

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